**TRANSMITTAL
FORM**

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Total Number of Pages in This Submission

Application Number

10/673,211

Filing Date

September 30, 2003

First Named Inventor

Xavier Montagne

Art Unit

2858

Examiner Name

TBA

Attorney Docket Number

003921.00136

ENCLOSURES (check all that apply)☐ Fee Transmittal Form☐ Fee Attached☐ Amendment / Reply☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☒ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Reply to Missing Parts/
Incomplete Application☐ Reply to Missing Parts
under 37 CFR 1.52 or 1.53☐ Drawing(s)☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a
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of Appeals and Interferences☐ Appeal Communication to TC
(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☐ Other Enclosure(s)
(please identify below):**Remarks****SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm

Banner & Witcoff, LTD.

Signature

Printed Name

Ross A. Dannenberg

Date

February 24, 2005

Reg.
No.

49,024

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Xavier Montagne et al.

Serial No.: 10/673,211

Filed: September 30, 2003

For: Testing Of Reconfigurable
Logic And Interconnect Sources

Group Art Unit: 2858

Examiner: TBA

Atty. Dkt. No. 003921.00136

INFORMATION DISCLOSURE STATEMENT

Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

In accordance with 37 C.F.R. § 1.56 and 1.97, Applicant(s) provides the following for consideration by the Office during examination of the above-captioned application. This submission is not intended as an admission that any submitted document or reference constitutes prior art against the claims of the present application. Applicant(s) do not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Applicant(s) hereby make the following statements, if checked:

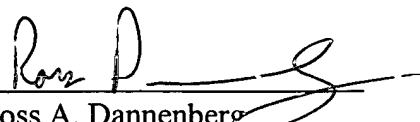
- ☒ Enclosed is a PTO Form PTO/SB/08a listing art for consideration by the Examiner, and a copy of each of the identified documents is included if presently required by Office rules and regulations.
- ☐ Pursuant to 37 C.F.R. § 1.97 (b) (1), the undersigned states that the present information disclosure statement is being filed within three months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. § 1.53 (d).

- ☐ Pursuant to 37 C.F.R. § 1.97 (b) (2), the undersigned states that the present information disclosure statement is being filed within three months of the date of entry of a national stage as set forth in 37 C.F.R. § 1.491 in an international application.
- ☒ Pursuant to 37 C.F.R. § 1.97 (b) (3), the undersigned states that the present information disclosure statement is being filed before the mailing of a first Office action.
- ☐ Pursuant to 37 C.F.R. § 1.97 (b) (4), the undersigned states that the present information disclosure statement is being filed before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. § 1.114.
- ☐ Pursuant to 37 C.F.R. § 1.97 (e) (1), the undersigned states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.
- ☐ Pursuant to 37 C.F.R. § 1.97 (e) (2), the undersigned states that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the undersigned, upon reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.
- ☐ Pursuant to 37 C.F.R. § 1.704 (d), the undersigned states that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application and that the communication was not received by any individual designated in § 1.56 (c) more than thirty days prior to the filing of this information disclosure statement.
- ☒ It is believed no fee is required to make this a complete and timely filing. However, if a fee is required, please charge our Deposit Account No. 19-0733.
- ☐ The undersigned hereby authorizes the Office to charge the fee pursuant to 37 C.F.R. § 1.17 (p) to our Deposit Account 19-0733.

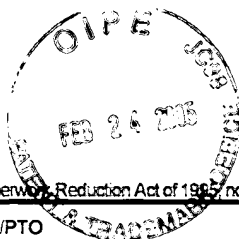
Consideration of this information is respectfully requested, with citation thereto on the face of the issued patent as applicable.

Respectfully submitted,
BANNER & WITCOFF, LTD.

Dated: Feb 24, 2005

By: 
Ross A. Dannenberg
Registration No. 49,024
1001 G. Street, N.W.
Washington, D.C. 20001-4597
(202) 824-3153

RAD/mmd



PTO/SB/33a (03-03)

Approved for use through 07/31/2008. OMB 0651-0331

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/673,211
Filing Date	September 30, 2003
First Named Inventor	Xavier Montagne
Art Unit	2858
Examiner Name	TBA
Attorney Docket Number	003921.00136

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US- 6,473,726	10-29-2002	Reblewski	
		US-6,265,894	07-24-2001	Reblewski et al.	
		US- 6,184,707	02-06-2001	Norman et al.	
		US- 5,821,773	10-13-1998	Norman et al.	
		US- 5,777,489	07-07-1998	Barbier et al.	
		US- 5,109,353	04-28-1992	Sample et al.	
		US- 5,036,473	07-30-1991	Butts et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		WO 94/06210	03-17-1994	Prabhakar Goel		
		EP 0 651 343	05-03-95	Quickturn Systems, Inc.		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		CHARLES CLOS, "A Study of Non-Blocking Switching Networks", The Bell System Technical Journal, March 1953, pages 406-424	
		WERNER ERHARD et al., "First Steps towards a Reconfigurable Asynchronous System", Friedrich-Schiller University Jena, Department of Computer Science, IEEE International Workshop on Rapid System Prototyping, June 1999, pages 28-31	
		FATIH KOCAN et al., "Concurrent D-Algorithm on Reconfigurable Hardware", IEEE 1999, pages 152-155	
		JACK JEAN et al., "Dynamic Reconfiguration to Support Concurrent Applications", IEEE Transactions Of Computers, Vol. 48, No. 6, June 1999, pages 591-602	
		BERNARD BOSI et al., "Reconfigurable Pipelined 2-D Convolvers for Fast Digital Signal Processing", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, No. 3, September 1999, pages 299-308	
		A. EJNIOUI et al., "Design Partitioning on Single-Chip Emulation Systems", Center for Microelectronics Research, Dept. of CSE, University of South Florida, 13 th International Conference on VLSI Design, 2000, pages 234-239	
		GREG SNIDER, "The Teramac Compiler", Hewlett-Packard, 1996, pages 1-51	
		Xilinx, "Programmable Gate Array Design Handbook", First Edition, 1986, pages i-A10	
		JONATHAN BABB, et al., "Logic Emulation with Virtual Wires", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, June 1997, pages 1-20	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

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